

## APPLICATION INFORMATION

Application Type::	Regular
Subject Matter::	Utility
CD-ROM or CD-R?::	None
Title::	VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR
Attorney Docket Number::	026-0044
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	
Total Drawing Sheets::	3
Small Entity?::	No
Secrecy Order in Parent Appl.?::	No

## APPLICANT INFORMATION

Applicant Authority Type::	Inventor
Primary Citizenship Country::	India
Status::	Full Capacity
Given Name::	Akhil K.
Family Name::	Garlapati
Name Suffix::	
City of Residence::	Boston
State or Province of Residence::	MA
Country of Residence::	USA
Street::	50 Jamaicaway, #3
City::	Boston
State or Province::	MA
Country::	USA
Postal or ZIP Code::	02130
Applicant Authority Type::	Inventor

Primary Citizenship Country:: USA  
Status:: Full Capacity  
Given Name:: Bruce P.  
Family Name:: Del Signore  
Name Suffix::  
City of Residence:: Hollis  
State or Province of Residence:: NH  
Country of Residence:: USA  
Street:: 31 Baldwin Lane  
City:: Hollis  
State or Province:: NH  
Country:: USA  
Postal or ZIP Code:: 03049

Applicant Authority Type:: Inventor  
Primary Citizenship Country:: USA  
Status:: Full Capacity  
Given Name:: David  
Family Name:: Pietruszynski  
Name Suffix::

City of Residence:: Austin  
State or Province of Residence:: TX  
Country of Residence:: USA  
Street:: 6211 John Chisum Lane  
City:: Austin  
State or Province:: TX  
Country:: USA  
Postal or ZIP Code:: 78749

#### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 22120

#### **DOMESTIC PRIORITY INFORMATION**

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

## **FOREIGN PRIORITY INFORMATION**

Country::                      Application No::                      Filing Date::

## **ASSIGNEE INFORMATION**

Assignee name::                      Silicon Laboratories, Inc.